

IN THE CLAIMS:

1. Canceled
2. (Currently Amended) A method as in claim [[1]] 4 wherein said display structure is a flat panel display structure and wherein said electrical signals drive pixels images of said flat panel display structure.
3. (Currently Amended) A method as in claim [[1]] 4 wherein said backplane is one of an active matrix array of thin films transistors and an active matrix array of diodes.
4. (Currently Amended) A method of fabricating a display structure comprising: as in
claim 1
coupling a frontplane to a backplane wherein a frontplane top surface laminates
to a backplane top surface, said frontplane and said backplane are fabricated separately,
said frontplane further includes a frontplane substrate and said backplane further
includes a backplane substrate, wherein;
a first electrode layer which is transparent is disposed over said frontplane
substrate;
a display medium which produces electro-optical effects upon a voltage
application is disposed over said first electrode;
a second electrode layer which is patterned is disposed over said display medium,
said second electrode layer includes a plurality of connecting regions; and

said backplane is electrically active to provide driving signals for said display medium wherein said backplane substrate includes a plurality of output pads to match said plurality of connecting regions; and

wherein said backplane substrate includes a plurality of functional blocks which further includes said plurality of output pads to match said plurality of connecting regions.

5. (Currently Amended) A method as in claim 4 wherein each of said plurality of functional blocks is a micro structure NanoBlock™.
6. (Currently Amended) A method as in claim [[1]] 4 wherein said display medium is an electroluminescent medium.
- 7 (Original) A method as in claim 6 wherein said electroluminescent medium is made of any one of organic light emitting diode organic materials, light emitting polymer materials, polymer light emitting diodes, and thin film electroluminescent displays.
8. (Currently Amended) A method as in claim [[1]] 4 wherein said display medium [[if]] is formed from a solid film.
9. (Currently Amended) A method as in claim [[1]] 4 further comprising:
coating said plurality of output pads with an insulation layer having plurality of vias.

10. (Currently Amended) A method of fabricating a display structure as in claim 9 further comprising :

coupling a frontplane to a backplane wherein a frontplane top surface laminates to a backplane top surface, said frontplane and said backplane are fabricated separately, said frontplane further includes a frontplane substrate and said backplane further includes a backplane substrate, wherein;

a first electrode layer which is transparent is disposed over said frontplane substrate;

a display medium which produces electro-optical effects upon a voltage application is disposed over said first electrode;

a second electrode layer which is patterned is disposed over said display medium, said second electrode layer includes a plurality of connecting regions; and

said backplane is electrically active to provide driving signals for said display medium wherein said backplane substrate includes a plurality of output pads to match said plurality of connecting regions;

coating said plurality of output pads with an insulation layer having plurality of vias; and

forming conductive adhesive deposits over said first plurality of vias, said conductive adhesive deposits establish conduct said driving signals.

11. (Currently Amended) A method as in claim [[1]] 4 wherein said second electrode layer is transparent.

12. (Currently Amended) A method as in claim [[1]] 4 wherein said first electrode layer is an anode layer made out of a transparent conducting material.

13. (Currently Amended) A method as in claim [[1]] 4 wherein said first electrode layer is made of a high workfunction material.

14. (Currently Amended) A method as in claim [[1]] 4 wherein said second electrode layer is made of a low workfunction material.

15. (Currently Amended) A method as in claim [[1]] 4 wherein said first electrode layer is any one of an indium tin oxide and a mixture of indium oxide and tin oxide material.

16. (Currently Amended) A method as in claim [[1]] 4 wherein said second electrode layer is made from any one of an aluminum, a calcium, and a magnesium.

17. (Currently Amended) A method as in claim [[1]] 4 wherein said frontplane substrate is made out of anyone of a silica, glass, transparent polymer, sapphire, quartz, and transparent plastic.

18. (Currently Amended) A method as in claim [[1]] 4 wherein said backplane substrate is made out of material selected from the group consisting of ceramic, glass, plastic, silicon wafer, gallium arsenide wafer, silica, and metal.

19. (Currently Amended) A method as in claim [[1]] 4 wherein said backplane substrate is any one of a rigid substrate and a flexible substrate.

20. (Currently Amended) A method as in claim [[9]] 4 further comprising:

coating a pressure sensitive conductive layer over said plurality of output pads;

treating said pressure sensitive conductor layer such that said pressure sensitive conductor layer includes conducting regions and nonconducting regions, said conducting regions matching with said plurality of output pads and said connecting regions.

21. (Currently Amended) A method as in claim 20 wherein said pressure sensitive conductor layer is a patternable pressure sensitive adhesive wherein said treating includes patterning patterning said patternable pressure sensitive adhesive.

22. (Original) A method as in claim 20 wherein said pressure sensitive conductor layer is a Z-direction conductive film conducting only in a perpendicular direction to a top surface and a bottom surface of said Z-direction conductive film.

23. Canceled

24. (Currently Amended) A display structure as in claim [[23]] 26 wherein said electroluminescent display structure is a flat panel display structure and wherein said electrical signals drive pixels of said flat panel display structure.

25. (Currently Amended) A display structure as in claim [[23]] 26 wherein said backplane is one of an active matrix array of thin films transistors and an active matrix array of diodes.

26. (Currently Amended) A display structure comprising: as in claim 23
- a frontplane coupling to a backplane, a first top surface of said frontplane laminates to a second top surface said backplane, said frontplane and said backplane are fabricated separately, said frontplane further includes a frontplane substrate and said backplane further includes a backplane substrate, wherein;
- a first electrode layer which is transparent is disposed over said frontplane substrate;
- a display medium which produces electro-optical effects upon a voltage application is disposed over said first electrode;
- a second electrode layer which is patterned is disposed over said display medium, said second electrode layer includes a plurality of connecting regions; and
- said backplane is electrically active to provide driving signals for said display medium wherein said backplane substrate includes a plurality of output pads to match said plurality of connecting regions; and
- wherein said backplane substrate includes a plurality of functional blocks which further includes said plurality of output pads to match said plurality of connecting regions.
27. (Currently Amended) A display structure as in claim [[23]] 26 wherein each of said plurality of functional blocks is a micro structure NanoBlock™.
28. (Currently Amended) A display structure as in claim [[23]] 26 wherein said display medium is an electroluminescent medium.

29. (Original) A display structure as in claim 28 wherein said electroluminescent medium is made of any one of organic light emitting diode organic materials, light emitting polymer materials, polymer light emitting diodes, and thin film electroluminescent displays.
30. (Currently Amended) A display structure as in claim [[23]] 26 wherein said display medium [[if]] is formed from a solid film.
31. (Currently Amended) A display structure as in claim [[23]] 26 further includes an insulation layer having a plurality of vias formed over said plurality of output pads.
32. (Currently Amended) A display structure as in claim 31 further includes comprising:
a frontplane coupling to a backplane, a first top surface of said frontplane laminates to a second top surface said backplane, said frontplane and said backplane are fabricated separately, said frontplane further includes a frontplane substrate and said backplane further includes a backplane substrate, wherein;
a first electrode layer which is transparent is disposed over said frontplane substrate;
a display medium which produces electro-optical effects upon a voltage application is disposed over said first electrode;
a second electrode layer which is patterned is disposed over said display medium, said second electrode layer includes a plurality of connecting regions; and said backplane is electrically active to provide driving signals for said display medium wherein said backplane substrate includes a plurality of output pads to match said plurality of connecting regions;

an insulation layer having a plurality of vias formed over said plurality of output pads; and

conductive adhesive deposits formed over said first plurality of vias, said conductive adhesive deposits conduct establish said driving signals.

33. (Currently Amended) A display structure as in claim [[23]] 26 wherein said second electrode layer is transparent.
34. (Currently Amended) A display structure as in claim [[23]] 26 wherein said first electrode layer is an anode layer made out of a transparent conducting material.
35. (Currently Amended) A display structure as in claim [[23]] 26 wherein said first electrode layer is any one of an indium tin oxide and a mixture of indium oxide and tin oxide.
36. (Currently Amended) A display structure as in claim [[23]] 26 wherein said first electrode layer is made of a high workfunction material.
37. (Currently Amended) A display structure as in claim [[23]] 26 wherein said second electrode is made of material having a low workfunction value.
38. (Currently Amended) A display structure as in claim [[23]] 26 wherein said second electrode layer is made from any one of an aluminum, a calcium, and a magnesium.

39. (Currently Amended) A display structure as in claim [[23]] 26 wherein said frontplane substrate is made out of anyone of a silica, glass, transparent polymer, sapphire, quartz, and transparent plastic.
40. (Currently Amended) A display structure as in claim [[23]] 26 wherein said backplane substrate is made out of material selected from the group consisting of ceramic, glass, plastic, silicon wafer, gallium arsenide wafer, silica, and metal.
41. (Currently Amended) A display structure as in claim [[23]] 26 wherein said backplane substrate is any one of a rigid substrate and a flexible substrate.
42. (Currently Amended) A display structure as in claim [[31]] 26 further comprising a pressure sensitive conductive layer coated over said plurality of output pads, said pressure sensitive conductor layer is treated such that said pressure sensitive conductor layer further includes conducting regions and nonconducting regions, said conducting regions matching with said plurality of output pads and said connecting regions.
43. (Original) A display structure as in claim 42 wherein said pressure sensitive conductor layer is a patternable pressure sensitive adhesive wherein said patternable pressure sensitive conductor layer is patterned to include said conducting regions and said nonconducting regions to match said plurality of said connecting regions.

44. (Original) A display structure as in claim 42 wherein said pressure sensitive conductor layer is a Z-direction conductive film conducting only in a perpendicular direction to a top surface and a bottom surface of said Z-direction conductive film.